

B' ✓ In the first operating mode according to Fig. 3A, four memory objects S1..S4 are arranged to form a first-in, first out structure (FIFO). Such a FIFO can be designed in a known manner as a ring buffer. In the first operating mode, which is also called the "FIFO operating mode" in the following text, the data objects D1..D3 to be transmitted are written via a write pointer WP, which is connected to the data transmitter, to the data regions B provided for this purpose in the memory objects S1..S4 in the FIFO. In this case, the first data object D1 is written to the data region B of the first memory object S1 in the FIFO. The respectively following data objects D2, D3 are stored in the data regions B of the correspondingly successive memory objects S2, S3. ✓

Replace the paragraph beginning at page 23, line 17, of the specification with the following:

B' ✓ A bus system 10 is designed, for example, to correspond to one of the bus systems 3, 4 shown in Fig. 4. The bus system 10, which in this case is a CAN network, has a multiplicity of network nodes 12, 12' connected to a data bus 11. The memory device 1, which is integrated in Fig. 5 in one of the network nodes 12', is in this case arranged between a first subscriber 3, which is a CAN module, and a second subscriber 4, which is a CPU module. The memory device 1 is in this case used for